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Search Results - Record(s) 1 through 13 of 13 returned.

☐ 1. Document ID: US 20020112144 A1

Using default format because multiple data bases are involved.

L2: Entry 1 of 13

File: PGPB

Aug 15, 2002

PGPUB-DOCUMENT-NUMBER: 20020112144

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020112144 A1

TITLE: APPARATUS AND METHOD FOR CODE-ENHANCED PERFORMANCE IN A DIGITAL SIGNAL PROCESSING UNIT

PUBLICATION-DATE: August 15, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
TESSAROLO, ALEXANDER	SYDNEY	TX	AU	
EHLIG, PETER N.	SUGARLAND	TX	US	
HOPKINS, GLENN HARLAND	MISSOURI CITY		US	
NATARAJAN, VENKATESH	BAGALORE		IN	

US-CL-CURRENT: 712/29

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 2. Document ID: US 20020007451 A1

L2: Entry 2 of 13

File: PGPB

Jan 17, 2002

PGPUB-DOCUMENT-NUMBER: 20020007451

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020007451 A1

TITLE: Central processing unit for easily testing and debugging programs

PUBLICATION-DATE: January 17, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Cho, Kyung Y.	Pusan		KR	
Lim, Jong Y.	Seoul		KR	

h e b b g e e e f e g b g e f b e

Lee, Geun T.	Seoul	KR
Han, Sang S.	Incheon	KR
Min, Byung G.	Kyungki-Do	KR
Lee, Heui	Kyungki-Do	KR

US-CL-CURRENT: [712/43](#); [712/227](#), [712/229](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 3. Document ID: US 20010004766 A1

L2: Entry 3 of 13

File: PGPB

Jun 21, 2001

PGPUB-DOCUMENT-NUMBER: 20010004766
PGPUB-FILING-TYPE: new-utility
DOCUMENT-IDENTIFIER: US 20010004766 A1

TITLE: System and method for distributed debugging and recording medium on which control programs are recorded

PUBLICATION-DATE: June 21, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Koyama, Kazuya	Tokyo		JP	

US-CL-CURRENT: [717/128](#); [714/38](#), [718/107](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 4. Document ID: US 6567933 B1

L2: Entry 4 of 13

File: USPT

May 20, 2003

US-PAT-NO: 6567933
DOCUMENT-IDENTIFIER: US 6567933 B1

TITLE: Emulation suspension mode with stop mode extension

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☒ 5. Document ID: US 6567910 B2

L2: Entry 5 of 13

File: USPT

May 20, 2003

US-PAT-NO: 6567910
DOCUMENT-IDENTIFIER: US 6567910 B2

TITLE: Digital signal processing unit with emulation circuitry and debug interrupt enable register indicating serviceable time-critical interrupts during real-time

h e b b g e e e f e g b g e f b e

emulation mode

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 6. Document ID: US 6564339 B1

L2: Entry 6 of 13

File: USPT

May 13, 2003

US-PAT-NO: 6564339

DOCUMENT-IDENTIFIER: US 6564339 B1

TITLE: Emulation suspension mode handling multiple stops and starts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 6557116 B1

L2: Entry 7 of 13

File: USPT

Apr 29, 2003

US-PAT-NO: 6557116

DOCUMENT-IDENTIFIER: US 6557116 B1

TITLE: Emulation suspension mode with frame controlled resource access

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 8. Document ID: US 6553513 B1

L2: Entry 8 of 13

File: USPT

Apr 22, 2003

US-PAT-NO: 6553513

DOCUMENT-IDENTIFIER: US 6553513 B1

TITLE: Emulation suspend mode with differing response to differing classes of interrupts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 9. Document ID: US 6067407 A

L2: Entry 9 of 13

File: USPT

May 23, 2000

US-PAT-NO: 6067407

DOCUMENT-IDENTIFIER: US 6067407 A

TITLE: Remote diagnosis of network device over a local area network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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h e b b g e e e f e g b g e f b e

☐ 10. Document ID: US 5881263 A

L2: Entry 10 of 13

File: USPT

Mar 9, 1999

US-PAT-NO: 5881263

DOCUMENT-IDENTIFIER: US 5881263 A

TITLE: Non-instruction base register addressing in a data processing apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 11. Document ID: US 5615371 A

L2: Entry 11 of 13

File: USPT

Mar 25, 1997

US-PAT-NO: 5615371

DOCUMENT-IDENTIFIER: US 5615371 A

TITLE: Debug processing system for user programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 12. Document ID: US 5535318 A

L2: Entry 12 of 13

File: USPT

Jul 9, 1996

US-PAT-NO: 5535318

DOCUMENT-IDENTIFIER: US 5535318 A

**** See image for Certificate of Correction ****

TITLE: Debugging system for a hierarchically structured page description language

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 13. Document ID: US 4853849 A

L2: Entry 13 of 13

File: USPT

Aug 1, 1989

US-PAT-NO: 4853849

DOCUMENT-IDENTIFIER: US 4853849 A

TITLE: Multi-tasking register set mapping system which changes a register set pointer block bit during access instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
STACK	351532
STACKS	76076
POINT\$3	0
POINT	3794226
POINTA	394
POINTAB	3
POINTABO	1
POINTAB6	1
POINTAC	1
POINTACL	3
POINTAC3	1
(L1 AND STACK NEAR3 POINT\$3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	13

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Term	Documents
STACK	351532
STACKS	76076
POINT\$3	0
POINT	3794226
POINTA	394
POINTAB	3
POINTABO	1
POINTAB6	1
POINTAC	1
POINTACL	3
POINTAC3	1
(L13 AND STACK NEAR4 POINT\$3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	13

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L14

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DATE: Friday, March 26, 2004 [Printable Copy](#) [Create Case](#)

Set
Name Query
side by
side

Hit
Count Set
Name
result set



DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L14</u>	L13 and stack near4 point\$3	13	<u>L14</u>
<u>L13</u>	debug\$5 near5 (enter\$3 or activat\$3 or switch\$5 or select\$3) near18 break\$1	119	<u>L13</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L12</u>	debug\$5 near5 (enter\$3 or activat\$3 or switch\$5 or select\$3) near18 break\$1	106	<u>L12</u>
<u>L11</u>	l5 and l1	23	<u>L11</u>
<u>L10</u>	l3 and l1	36	<u>L10</u>
<u>L9</u>	l3 and l1L8	0	<u>L9</u>
<u>L8</u>	l2 and l6	6	<u>L8</u>
<u>L7</u>	l1 and L6	48	<u>L7</u>
<u>L6</u>	l4 or L5	14471	<u>L6</u>
<u>L5</u>	(717/127-161)![CCLS]	3057	<u>L5</u>
<u>L4</u>	(714/1-57)[CCLS]	11752	<u>L4</u>
<u>L3</u>	(714/1-57)![CCLS]	11752	<u>L3</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L2</u>	L1 and stack near3 point\$3	13	<u>L2</u>
<u>L1</u>	debug\$5 near5 (enter\$3 or activat\$3 or switch\$5 or select\$3) near9 break\$1	103	<u>L1</u>

END OF SEARCH HISTORY

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Search Results - Record(s) 1 through 19 of 19 returned.

☐ 1. Document ID: US 6708289 B1

L2: Entry 1 of 19

File: USPT

Mar 16, 2004

US-PAT-NO: 6708289

DOCUMENT-IDENTIFIER: US 6708289 B1

TITLE: Microcomputer, electronic equipment and debugging system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 2. Document ID: US 6665821 B1

L2: Entry 2 of 19

File: USPT

Dec 16, 2003

US-PAT-NO: 6665821

DOCUMENT-IDENTIFIER: US 6665821 B1

TITLE: Microcomputer, electronic equipment, and debugging system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 3. Document ID: US 6567933 B1

L2: Entry 3 of 19

File: USPT

May 20, 2003

US-PAT-NO: 6567933

DOCUMENT-IDENTIFIER: US 6567933 B1

TITLE: Emulation suspension mode with stop mode extension

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 4. Document ID: US 6567910 B2

L2: Entry 4 of 19

File: USPT

May 20, 2003

US-PAT-NO: 6567910

DOCUMENT-IDENTIFIER: US 6567910 B2

TITLE: Digital signal processing unit with emulation circuitry and debug interrupt enable register indicating serviceable time-critical interrupts during real-time emulation mode

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 5. Document ID: US 6564339 B1

L2: Entry 5 of 19

File: USPT

May 13, 2003

US-PAT-NO: 6564339

DOCUMENT-IDENTIFIER: US 6564339 B1

TITLE: Emulation suspension mode handling multiple stops and starts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 6. Document ID: US 6557116 B1

L2: Entry 6 of 19

File: USPT

Apr 29, 2003

US-PAT-NO: 6557116

DOCUMENT-IDENTIFIER: US 6557116 B1

TITLE: Emulation suspension mode with frame controlled resource access

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 7. Document ID: US 6553513 B1

L2: Entry 7 of 19

File: USPT

Apr 22, 2003

US-PAT-NO: 6553513

DOCUMENT-IDENTIFIER: US 6553513 B1

TITLE: Emulation suspend mode with differing response to differing classes of interrupts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☒ 8. Document ID: US 6553506 B1

L2: Entry 8 of 19

File: USPT

Apr 22, 2003

US-PAT-NO: 6553506

DOCUMENT-IDENTIFIER: US 6553506 B1

TITLE: Information processing device and electronic equipment

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☒ 9. Document ID: US 6223275 B1

L2: Entry 9 of 19

File: USPT

Apr 24, 2001

US-PAT-NO: 6223275

DOCUMENT-IDENTIFIER: US 6223275 B1

TITLE: Microprocessor with reduced instruction set limiting the address space to upper 2 Mbytes and executing a long type register branch instruction in three intermediate instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 10. Document ID: US 6067407 A

L2: Entry 10 of 19

File: USPT

May 23, 2000

US-PAT-NO: 6067407

DOCUMENT-IDENTIFIER: US 6067407 A

TITLE: Remote diagnosis of network device over a local area network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 11. Document ID: US 5903718 A

L2: Entry 11 of 19

File: USPT

May 11, 1999

US-PAT-NO: 5903718

DOCUMENT-IDENTIFIER: US 5903718 A

**** See image for Certificate of Correction ****

TITLE: Remote program monitor method and system using a system-under-test microcontroller for self-debug

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 12. Document ID: US 5881263 A

L2: Entry 12 of 19

File: USPT

Mar 9, 1999

US-PAT-NO: 5881263

DOCUMENT-IDENTIFIER: US 5881263 A

TITLE: Non-instruction base register addressing in a data processing apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 13. Document ID: US 5615371 A

L2: Entry 13 of 19

File: USPT

Mar 25, 1997

US-PAT-NO: 5615371

DOCUMENT-IDENTIFIER: US 5615371 A

TITLE: Debug processing system for user programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 14. Document ID: US 5535318 A

L2: Entry 14 of 19

File: USPT

Jul 9, 1996

US-PAT-NO: 5535318

DOCUMENT-IDENTIFIER: US 5535318 A

**** See image for Certificate of Correction ****

TITLE: Debugging system for a hierarchically structured page description language

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 15. Document ID: US 4853849 A

L2: Entry 15 of 19

File: USPT

Aug 1, 1989

US-PAT-NO: 4853849

DOCUMENT-IDENTIFIER: US 4853849 A

TITLE: Multi-tasking register set mapping system which changes a register set pointer block bit during access instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 16. Document ID: US 4525780 A

L2: Entry 16 of 19

File: USPT

Jun 25, 1985

US-PAT-NO: 4525780

DOCUMENT-IDENTIFIER: US 4525780 A

TITLE: Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 17. Document ID: US 4493027 A

L2: Entry 17 of 19

File: USPT

Jan 8, 1985

US-PAT-NO: 4493027

DOCUMENT-IDENTIFIER: US 4493027 A

TITLE: Method of performing a call operation in a digital data processing system having microcode call and return operations

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 18. Document ID: US 4455602 A

L2: Entry 18 of 19

File: USPT

Jun 19, 1984

US-PAT-NO: 4455602

DOCUMENT-IDENTIFIER: US 4455602 A

TITLE: Digital data processing system having an I/O means using unique address providing and access priority control techniques

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 19. Document ID: US 4445177 A

L2: Entry 19 of 19

File: USPT

Apr 24, 1984

US-PAT-NO: 4445177

DOCUMENT-IDENTIFIER: US 4445177 A

TITLE: Digital data processing system utilizing a unique arithmetic logic unit for handling uniquely identifiable addresses for operands and instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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Term	Documents
STACK	141427
STACKS	36887
POINT\$3	0
POINT	1510432
POINTA	9
POINTADR	5
POINTAGE	19

POINTAL	2
POINTAN	2
POINTAND	14
POINTARE	1
(STACK NEAR4 POINT\$3 AND L1).USPT.	19

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STACK	141427
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POINT\$3	0
POINT	1510432
POINTA	9
POINTADR	5
POINTAGE	19
POINTAL	2
POINTAN	2
POINTAND	14
POINTARE	1
(STACK NEAR4 POINT\$3 AND L1).USPT.	19

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Search:

L2

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Interrupt

Search History

DATE: Friday, March 26, 2004 [Printable Copy](#) [Create Case](#)

Set
Name **Query**
 side by
 side

Hit
Count

Set
Name
 result set

DB=USPT; PLUR=YES; OP=OR



L2 stack near4 point\$3 and L1

19 L2

L1 debug\$4 near5 (switch\$5 or enter\$3 or enabl\$3 or activat\$4) near20
break\$3

101 L1

END OF SEARCH HISTORY